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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

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Complete if Known

Application Number	10/688,461
Filing Date	October 17, 2003
First Named Inventor	Meyer, James
Group Art Unit	2661
Examiner Name	Unknown

Attorney Docket No: 303.858US1

**US PATENT DOCUMENTS**

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate
JY	US-2002/0144085-A1	10/03/2002	Hiratsuka, S. , et al.	03/19/2002

**FOREIGN PATENT DOCUMENTS**

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JY	WO-02084428A2	10/24/2002	Lee, T. R., et al.	
JY	WO-05038660A2	04/28/2005	Meyer, J. W., et al.	
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JY	WO-9506285A2	03/02/1995	Petersen, B.	

**OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>1</sup>
JY		"IEEE Standard for High-Bandwidth Memory Interface Based on Scalable Coherent Interface(SCI) Signaling Technology (RAMLink)", IEEE Std 1596.4-1996, (1996), 1-91	

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Substitute Disclosure Statement Form (PTO-1449)

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Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
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## **OTHER DOCUMENTS – NON PATENT LITERATURE DOCUMENTS**

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